Fault-Tolerance Techniques for SRAM-Based FPGAs (Frontiers in Electronic Testing)

Fernanda Lima Kastensmidt, Ricardo Reis

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This book reviews fault-tolerance techniques for SRAM-based Field Programmable Gate Arrays (FPGAs), outlining many methods for designing fault tolerance systems. Some of these are based on new fault-tolerant architecture, and others on protecting the high-level hardware description before synthesis in the FPGA. The text helps the reader choose the best techniques project-by-project, and to compare fault tolerant techniques for programmable logic applications.

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